# 40V, 100A, 3mΩ N-channel Power Trench MOSFET

### JMTG035N04L

#### **Features**

- $\bullet \;\;$  Excellent  $R_{DS(ON)}$  and Low Gate Charge
- 100% UIS Tested
- 100% ΔVds Tested
- Halogen-free; RoHS-compliant
- Pb-free plating

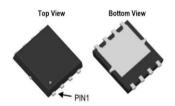
### **Applications**

- Load Switch
- PWM Application
- Power Management

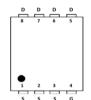
### **Product Summary**

Parameters	Value	Unit
$V_{DSS}$	40	V
$V_{GS(th)\_Typ}$	1.5	V
$I_D(@V_{GS}=10V)$	100	Α
$R_{DS(ON)\_Typ}(@V_{GS}=10V$	2.4	mΩ
$R_{DS(ON)\_Typ}(@V_{GS}=4.5V$	3.0	mΩ

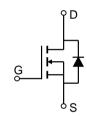




**PDFN 5X6 -8L** 



**Pin Assignment** 



**Schematic Diagram** 

#### **Ordering Information**

Device	Marking	MSL	Form	Package	Reel(pcs)	Per Carton (pcs)
JMTG035N04L	G035N04L	1	Tape&Reel	PDFN5x6-8L	5000	50000

### Absolute Maximum Ratings (@ T<sub>C</sub> = 25°C unless otherwise specified)

Symbol	Parameter		Value	Unit
$V_{DS}$	Drain-to-Source Voltage		40	V
$V_{GS}$	Gate-to-Source Voltage		±20	V
L	Continuous Drain Current	$T_C = 25$ °C	100	A
I <sub>D</sub>	Continuous Drain Current	$T_C = 100$ °C	63	^
I <sub>DM</sub>	Pulsed Drain Current (1)		Refer to Fig.4	Α
E <sub>AS</sub>	Single Pulsed Avalanche Energ	y <sup>(2)</sup>	306	mJ
$P_{D}$	Power Dissipation	$T_C = 25^{\circ}C$	125	W
·В		$T_C = 100$ °C	50	VV
$T_{J}, T_{STG}$	Junction & Storage Temperature F	Range	-55 to 150	°C

#### **Thermal Characteristics**

	Symbol	Parameter	Max	Unit		
	$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (3)	37	°C/W		
	Raic	Thermal Resistance, Junction to Case	1.0	C/VV		



## **Electrical Characteristics** (T<sub>J</sub> = 25°C unless otherwise specified)

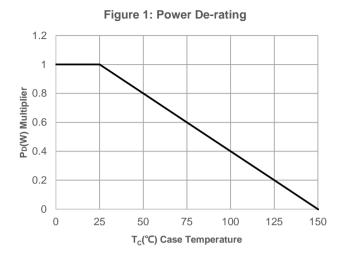
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Off Cha	racteristics				ı	
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	40	•	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{DS} = 40V, V_{GS} = 0V$	-	-	1.0	μА
I <sub>GSS</sub>	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA
On Cha	racteristics					
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.0	1.5	2.5	V
R	Static Drain-Source ON-Resistance <sup>(4)</sup>	$V_{GS} = 10V, I_D = 30A$	1	2.4	2.9	mΩ
R <sub>DS(ON)</sub>	Static Drain-Source ON-Resistance	$V_{GS} = 4.5V, I_D = 20A$	-	3.0	4.5	mΩ
Dynami	ic Characteristics					
$R_{g}$	Gate Resistance	f = 1MHz	-	1.3	-	Ω
C <sub>iss</sub>	Input Capacitance	.,	4376	6126	8270	pF
C <sub>oss</sub>	Output Capacitance	$V_{GS} = 0V$ , $V_{DS} = 20V$ , $f = 1MHz$	358	501	677	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	1 – 1101112	249	348	470	pF
$Q_g$	Total Gate Charge		79	111	150	nC
$Q_{gs}$	Gate Source Charge	$V_{GS} = 0 \text{ to } 10V$ $V_{DS} = 20V, I_D = 30A$	14	20	27	nC
$Q_{gd}$	Gate Drain("Miller") Charge	V DS = 20 V, ID = 30A	15	20	27	nC
Switchi	ng Characteristics					
t <sub>d(on)</sub>	Turn-On DelayTime		-	14	-	ns
t <sub>r</sub>	Turn-On Rise Time	$V_{GS} = 10V, V_{DD} = 19.5V$	-	28	-	ns
$t_{d(off)}$	Turn-Off DelayTime	$I_D = 30A, R_{GEN} = 3\Omega$	-	77	-	ns
t <sub>f</sub>	Turn-Off Fall Time		ı	23	-	ns
<b>Body D</b>	iode Characteristics					
Is	Maximum Continuous Body Diode Forward (	Current	-	-	100	Α
I <sub>SM</sub>	Maximum Pulsed Body Diode Forward Current			•	399	Α
V <sub>SD</sub>	Body Diode Forward Voltage	$V_{GS} = 0V, I_{S} = 30A$	-		1.2	V
trr	Body Diode Reverse Recovery Time	I <sub>F</sub> = 30A, di/dt = 100A/us	18	25	33	ns
Qrr	Body Diode Reverse Recovery Charge	$r_F = 30A$ , $ui/ui = 100A/us$	-	16	-	nC

Notes:

- ${\bf 1.}\ {\bf Repetitive}\ {\bf Rating:}\ {\bf Pulse}\ {\bf Width}\ {\bf Limited}\ {\bf by}\ {\bf Maximum}\ {\bf Junction}\ {\bf Temperature}.$
- $2.~E_{AS}~condition:~Starting~T_J=25C,~V_{DD}=15V,~V_G=10V,~R_G=25ohm,~L=0.5mH,~I_{AS}=35.56A,~V_{DD}=0V~during~time~in~avalanche.$
- 3.  $R_{\theta JA}$  is measured with the device mounted on a 1inch² pad of 2oz copper FR4 PCB.
- 4. Pulse Test: Pulse Width  $\leq$  300  $\mu$ s, Duty Cycle  $\leq$  0.5%.



# **Typical Performance Characteristics**



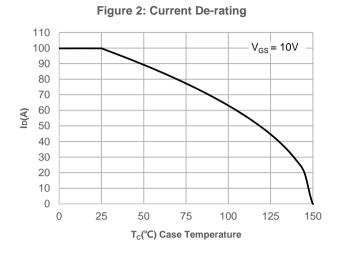
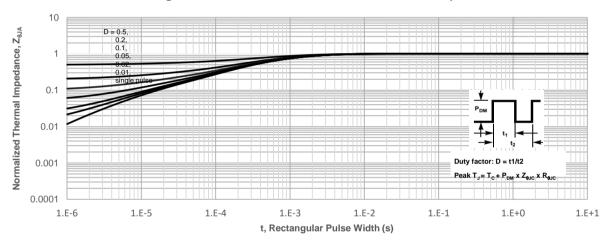
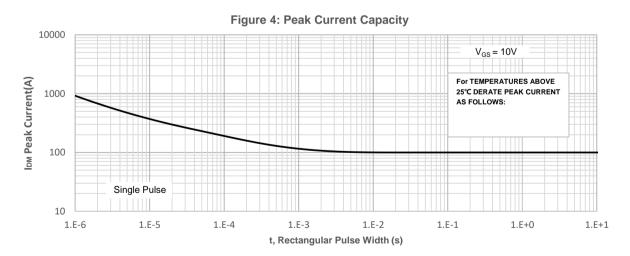


Figure 3: Normalized Maximum Transient Thermal Impedance







# **Typical Performance Characteristics**

Figure 5: Output Characteristics

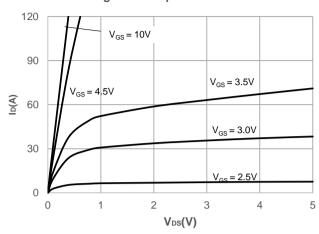


Figure 6: Typical Transfer Characteristics

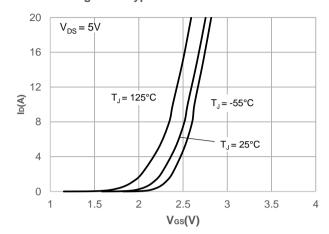


Figure 7: On-resistance vs. Drain Current

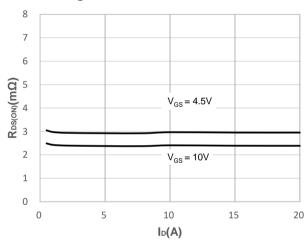


Figure 8: Body Diode Characteristics

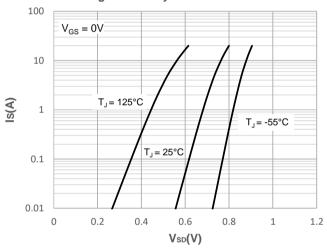


Figure 9: Gate Charge Characteristics

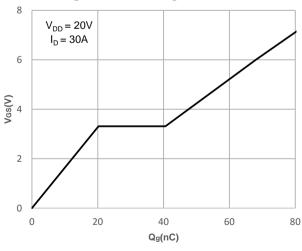
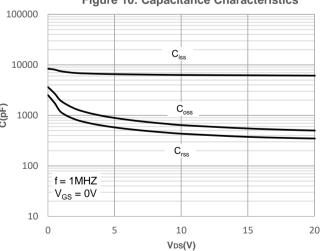


Figure 10: Capacitance Characteristics





# **Typical Performance Characteristics**

Figure 11: Normalized Breakdown voltage vs.
Junction Temperature

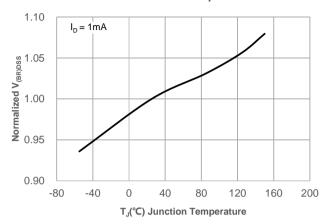


Figure 13: Normalized Threshold Voltage vs.
Junction Temperature

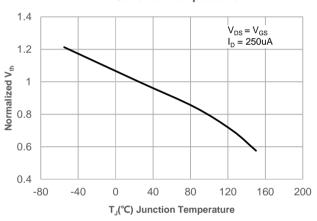


Figure 15: Maximum Safe Operating Area

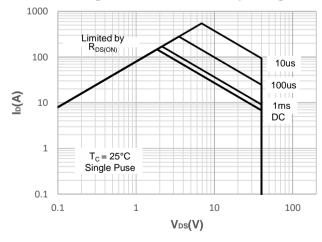
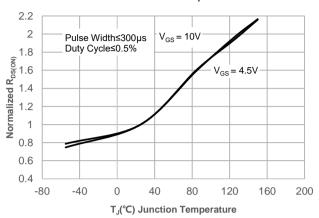
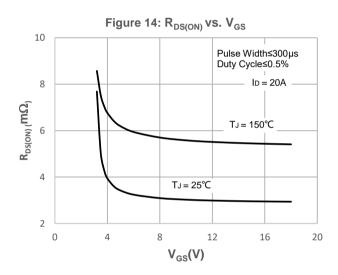


Figure 12: Normalized on Resistance vs.
Junction Temperature







### **Test Circuit**

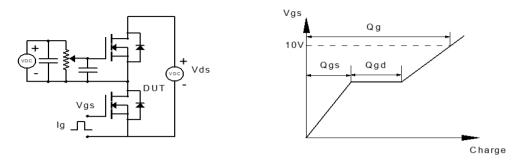


Figure 1: Gate Charge Test Circuit & Waveform

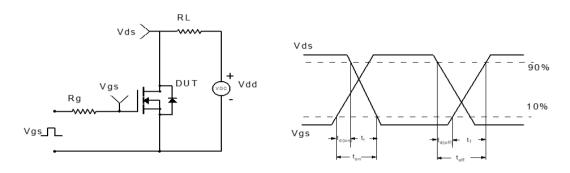


Figure 2: Resistive Switching Test Circuit & Waveform

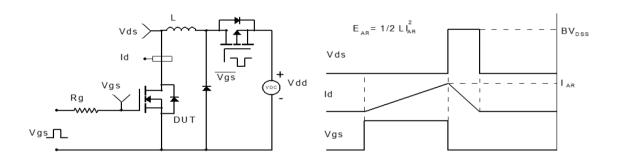


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

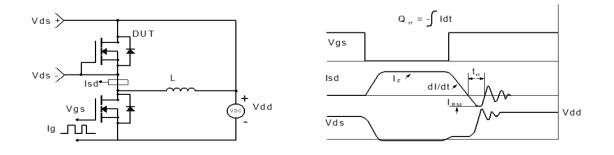
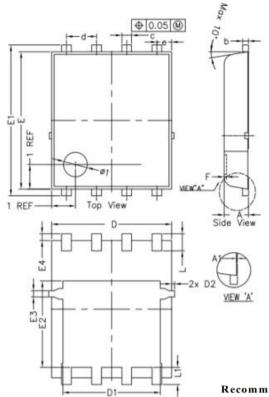


Figure 4: Diode Recovery Test Circuit & Waveform



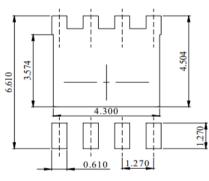
## Package Mechanical Data(PDFN 5X6-8L)



Bottom View

CULTURAL C	DIN	IENSION IN	MM	DIME	NSION IN I	NCHES
SYMBOLS	MIN	NOM	MAX	MIN	NOM	MAX
* A	0.900	1.000	1.100	0.035	0.039	0.043
A1	0.000	***	0.050	0.000	****	0.002
b	0.246	0.254	0.312	0.010	0.010	0.012
* c	0.310	0.410	0.510	0.012	0.016	0.020
d		1.27 BSC			0.050 BSC	Ġ.
* D	4.950	5.050	5.150	0.195	0.199	0.203
D1	4.000	4.100	4.200	0.157	0.161	0.165
* D2	***	***	0.125	***	***	0.005
e		0.62 BSC		0.024 BSC		
* E	5.500	5.600	5.700	0.217	0.220	0.224
* E1	6.050	6.150	6.250	0.238	0.242	0.246
E2	3.425	3.525	3.625	0.135	0.139	0.143
E3	0.150	0.250	0.350	0.006	0.010	0.014
* E4	0.175	0.275	0.375	0.007	0.011	0.015
F	-	-	0.100	-	-	0.004
* L	0.500	0.600	0.700	0.02	0.02	0.03
L1	0.600	0.700	0.800	0.02	0.03	0.03

Recommended Soldering Footprint



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